

438	14	Subclass
		Class
		ISSUE Q

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

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SCANNED S.O. Q.A. ALI

APPLICATION NO. 09/869274	CONT/PRIOR D	CLASS 438	SUBCLASS 108	ART UNIT 2823	EXAMINER Coleman W. R. R. R.
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## APPLICANTS

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Semiconductor integrated circuit and manufacture thereof

**FILE**

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.      Figs. Drwg.      Print Fig.		<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)      (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner)      (Date)		<b>ISSUE FEE</b>	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)      (Date)		<b>ISSUE BATCH NUMBER</b>	

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